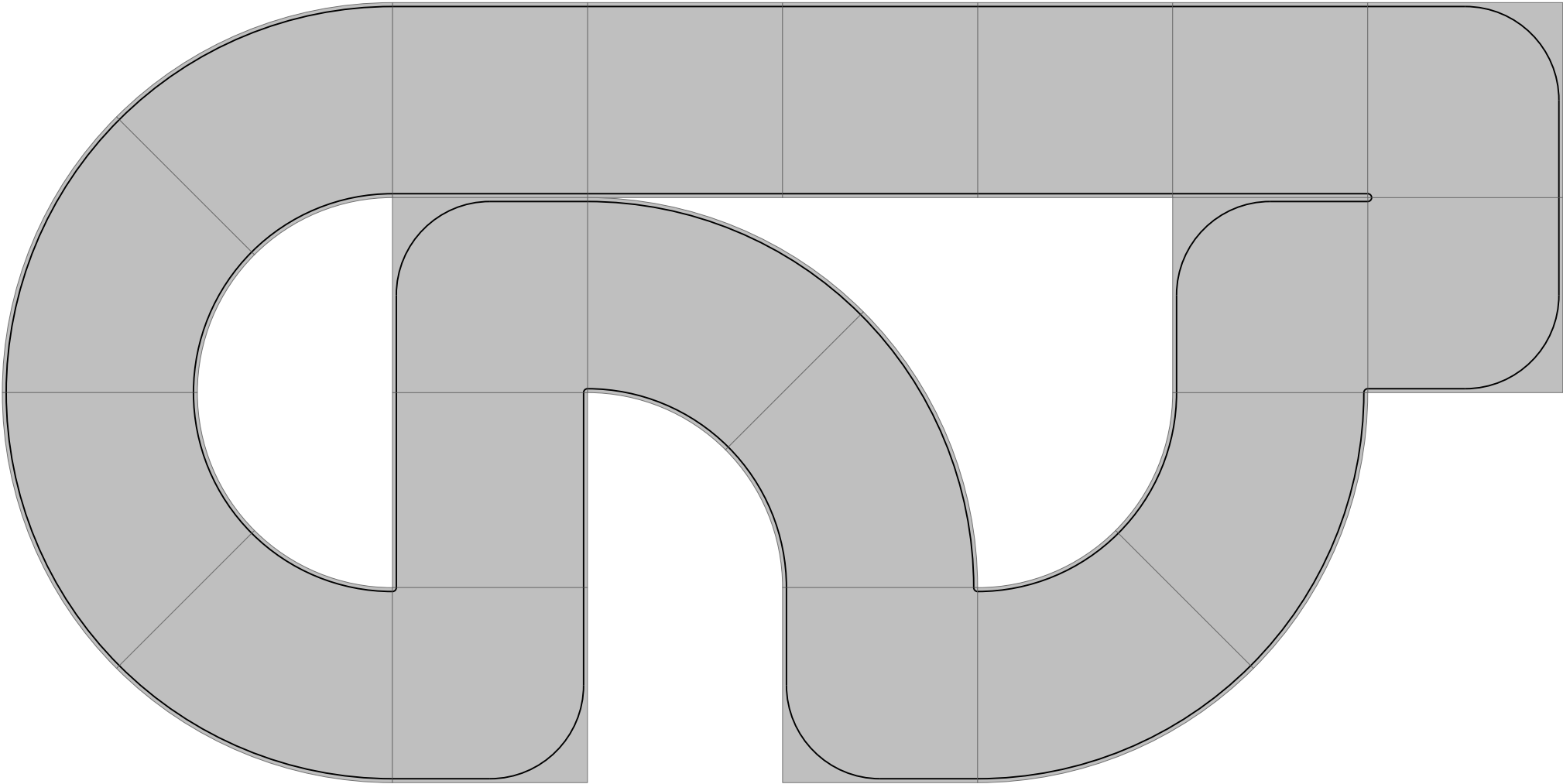
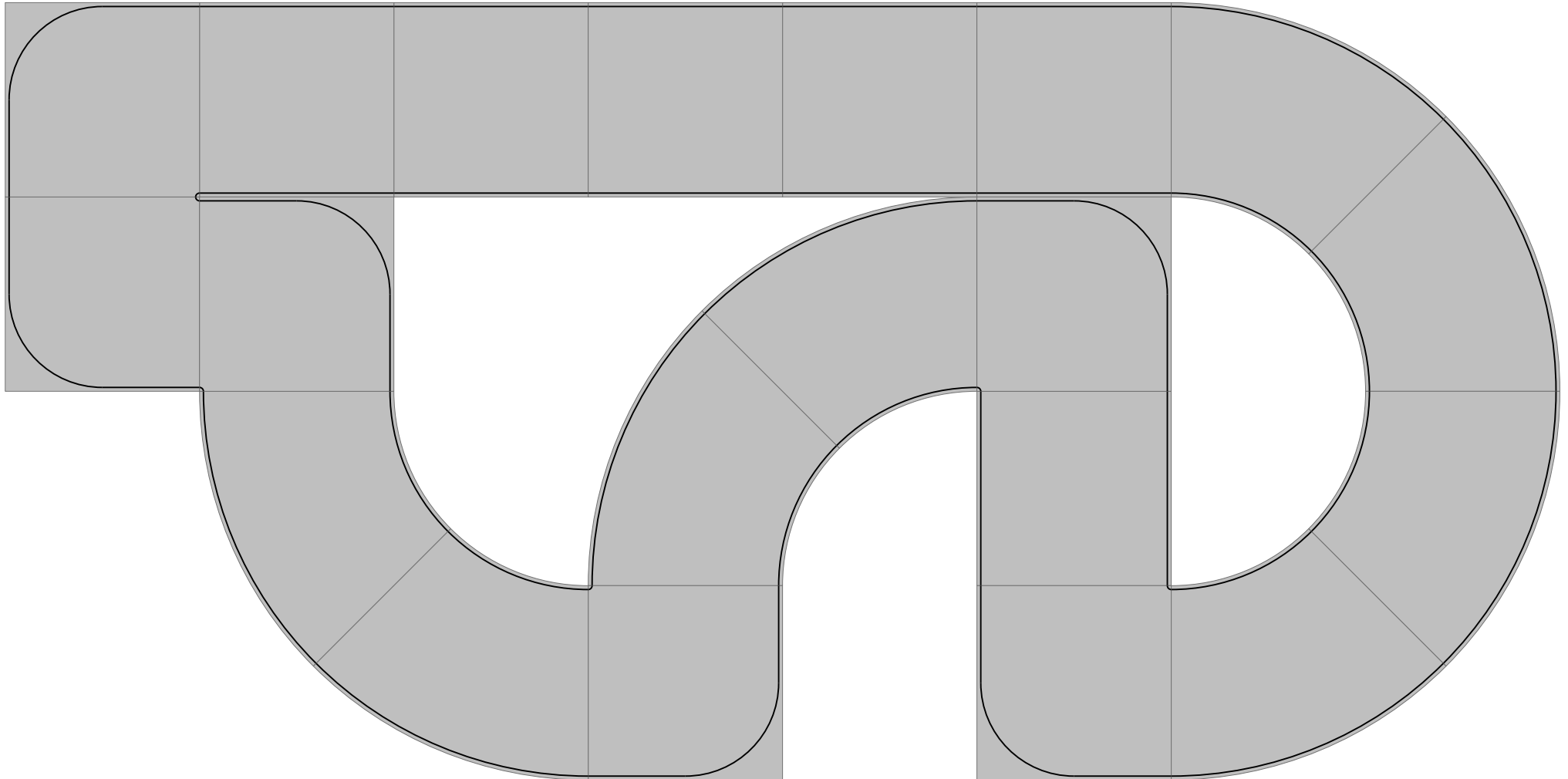
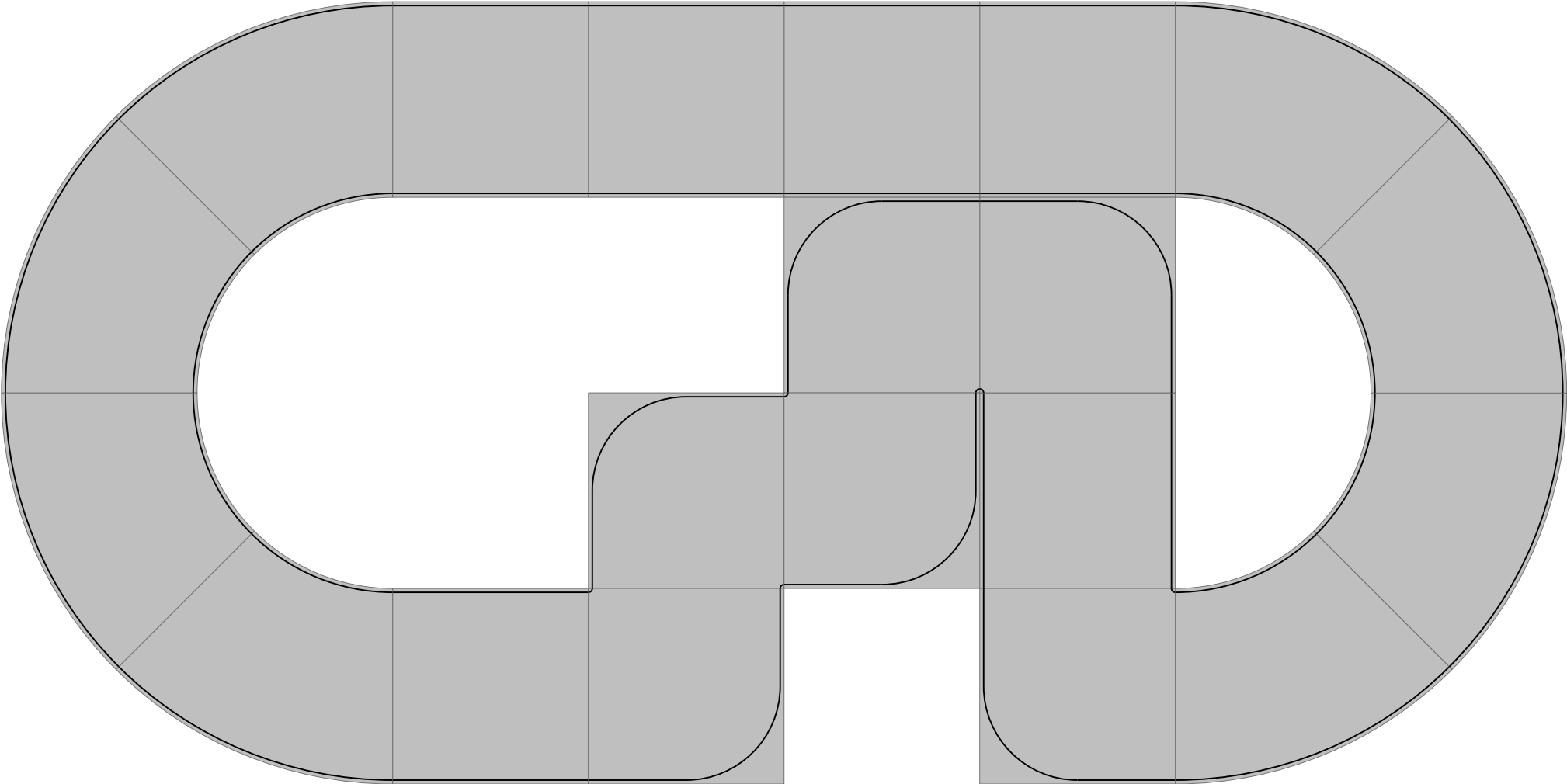


Circuit 1

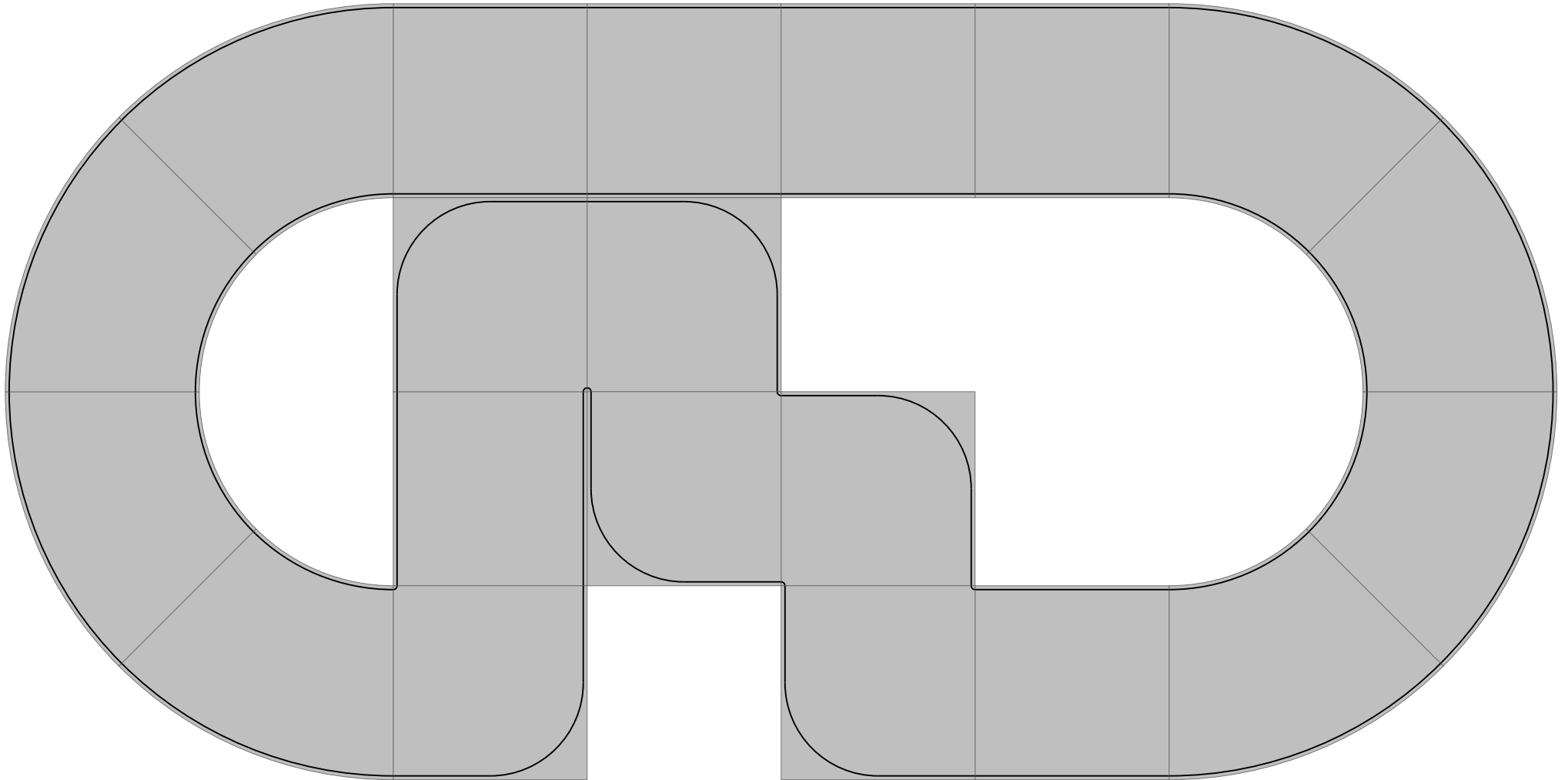


Circuit 1  
v.2

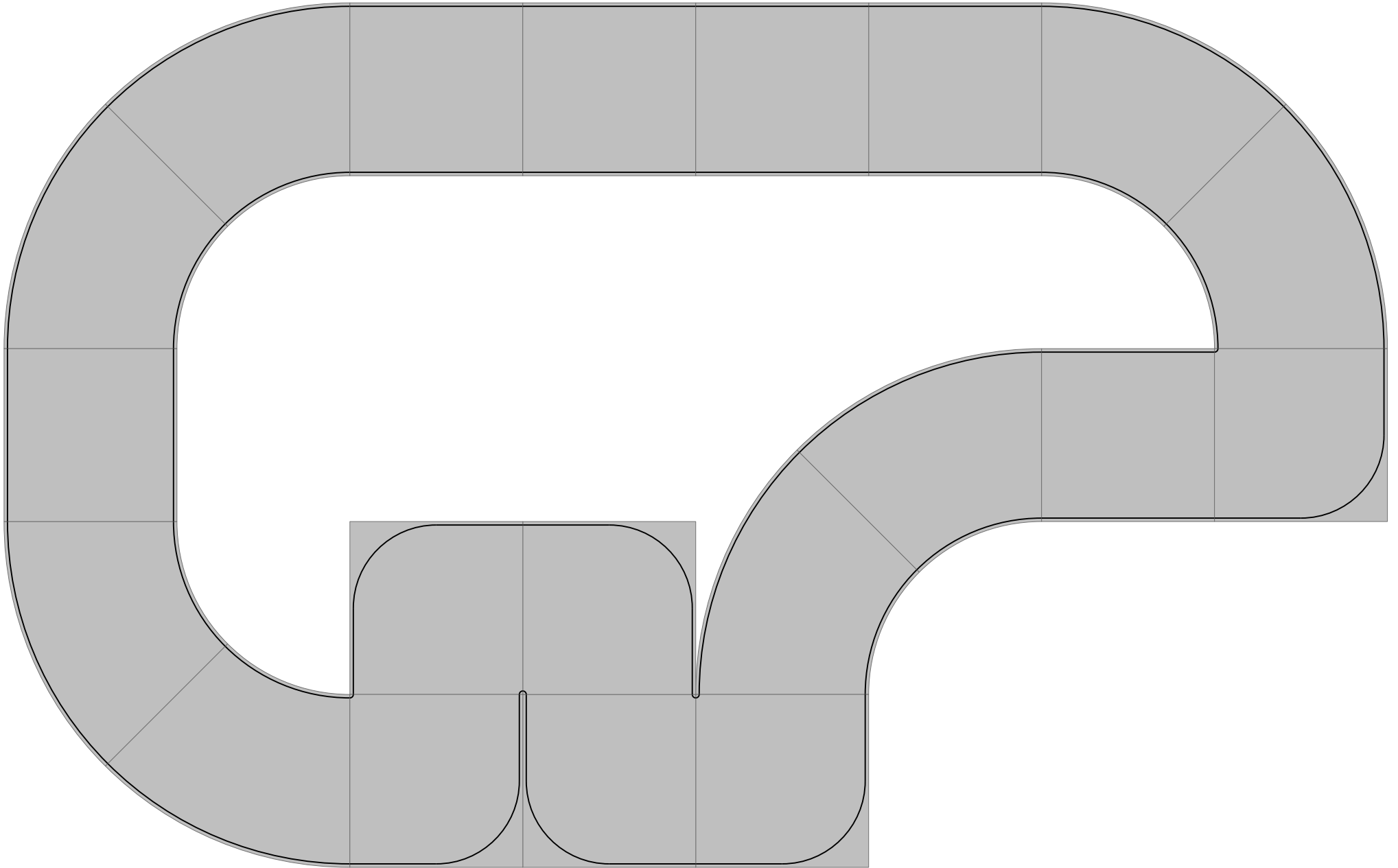




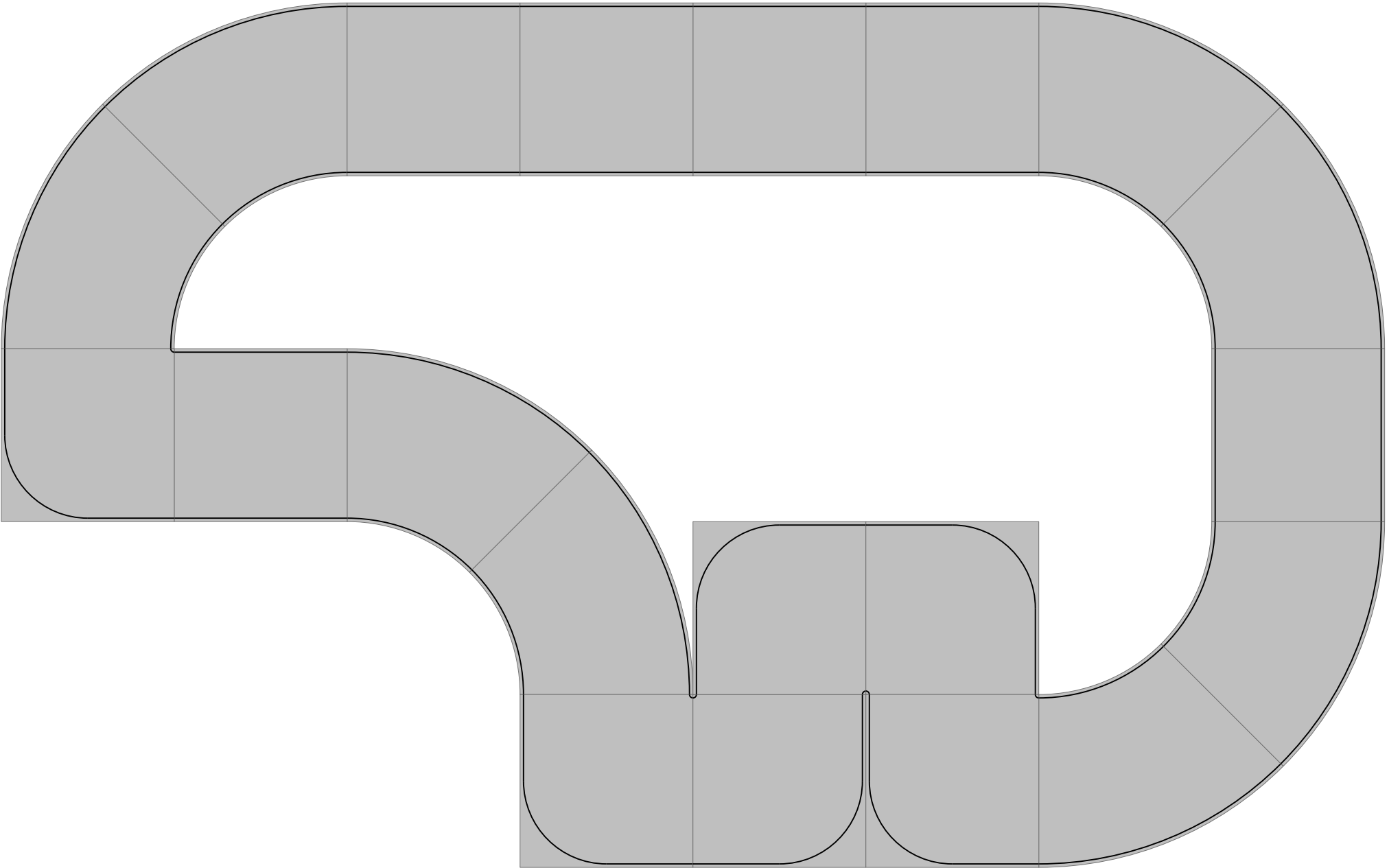
Circuit 2  
v.2



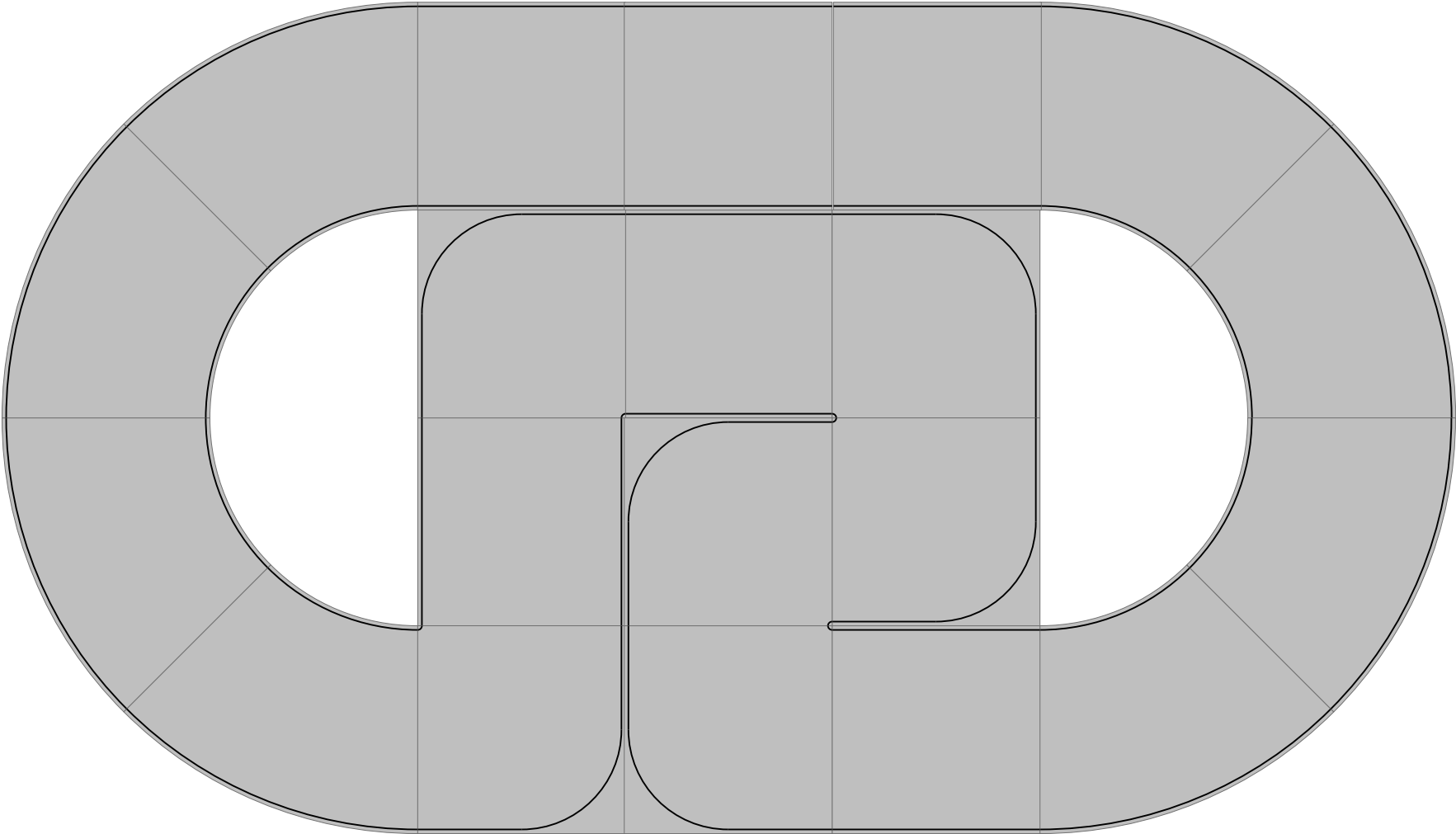
Circuit 3



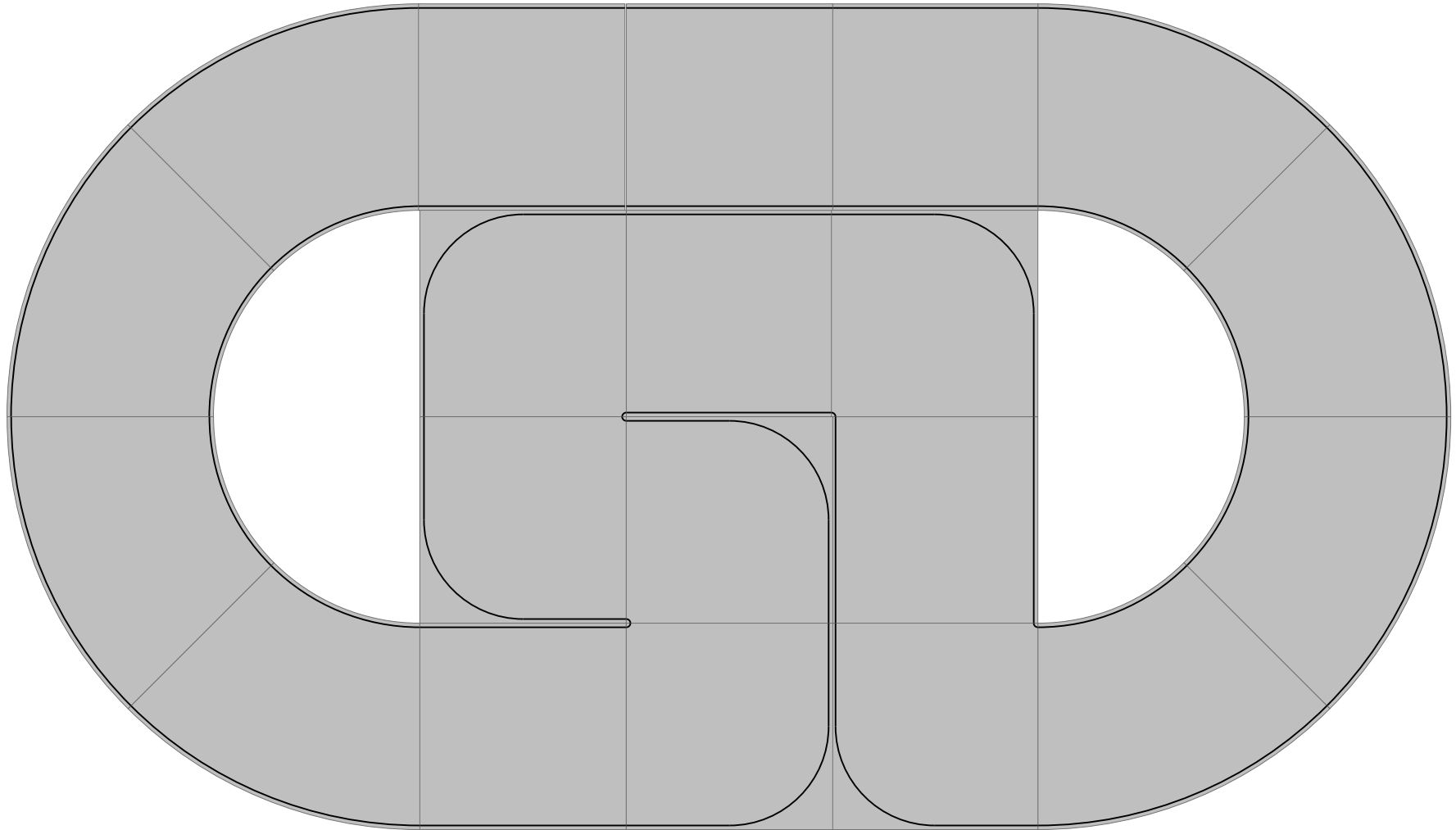
Circuit 3  
v.2



Circuit 4

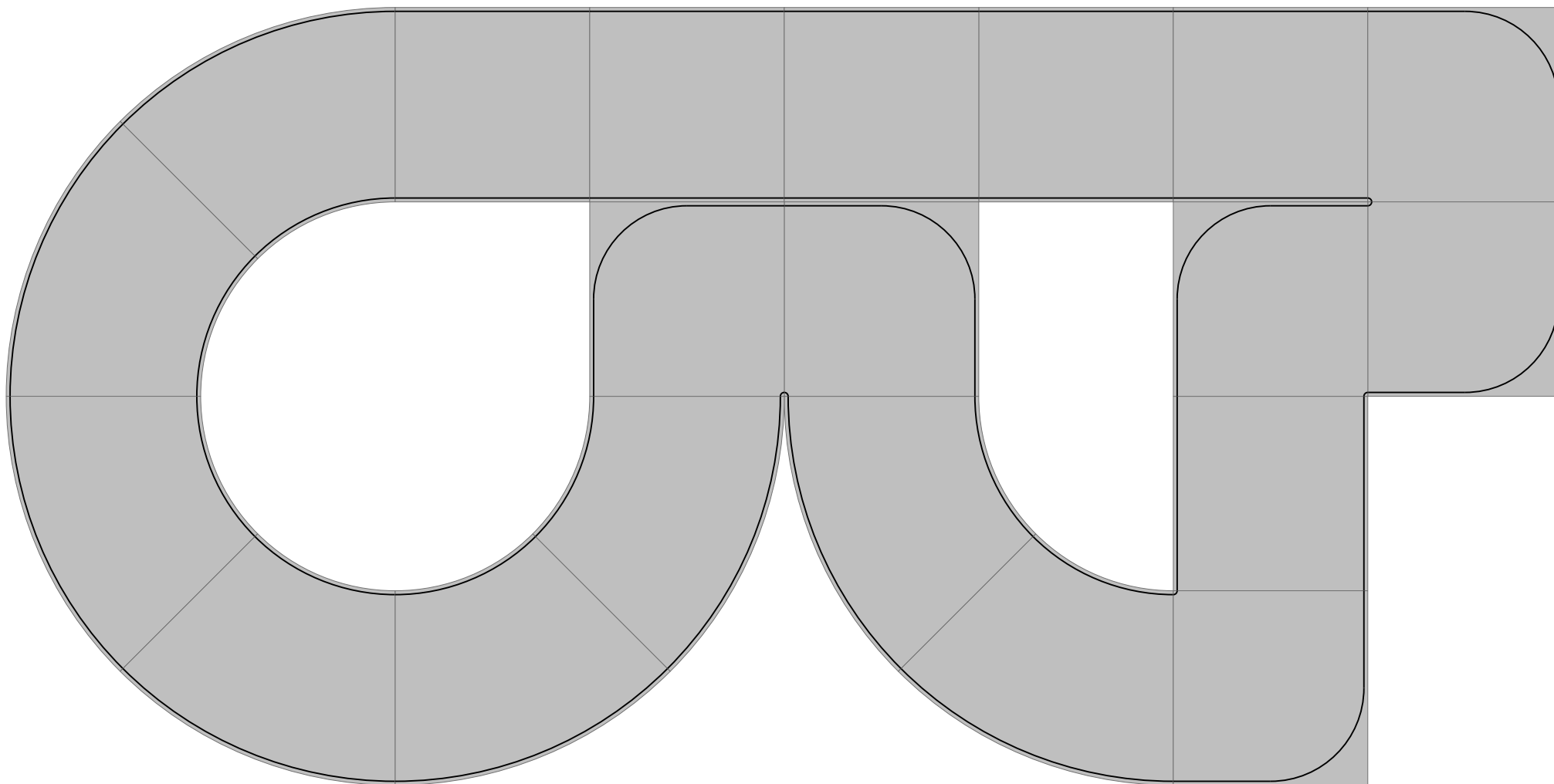


Circuit 4  
v.2

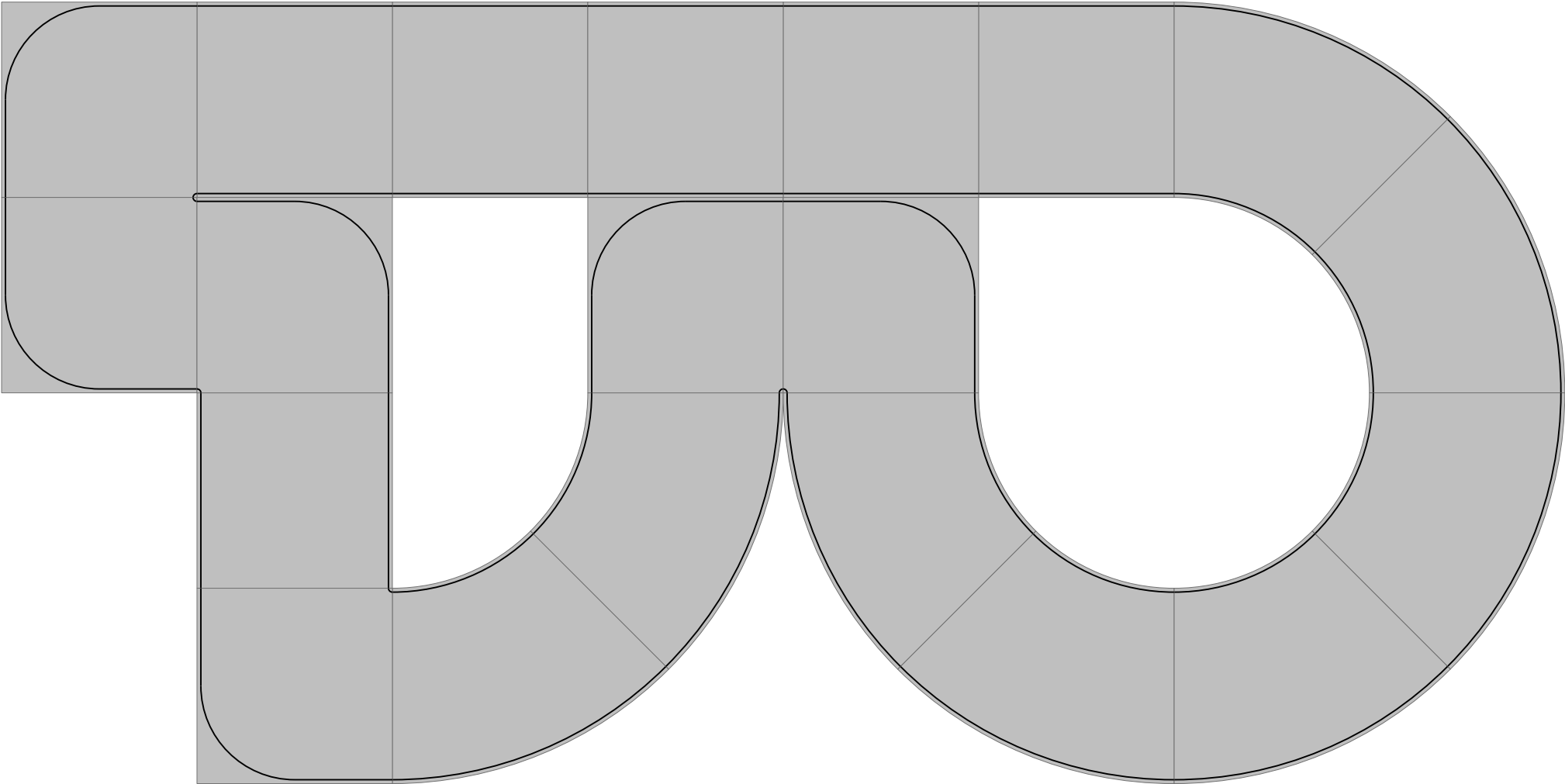




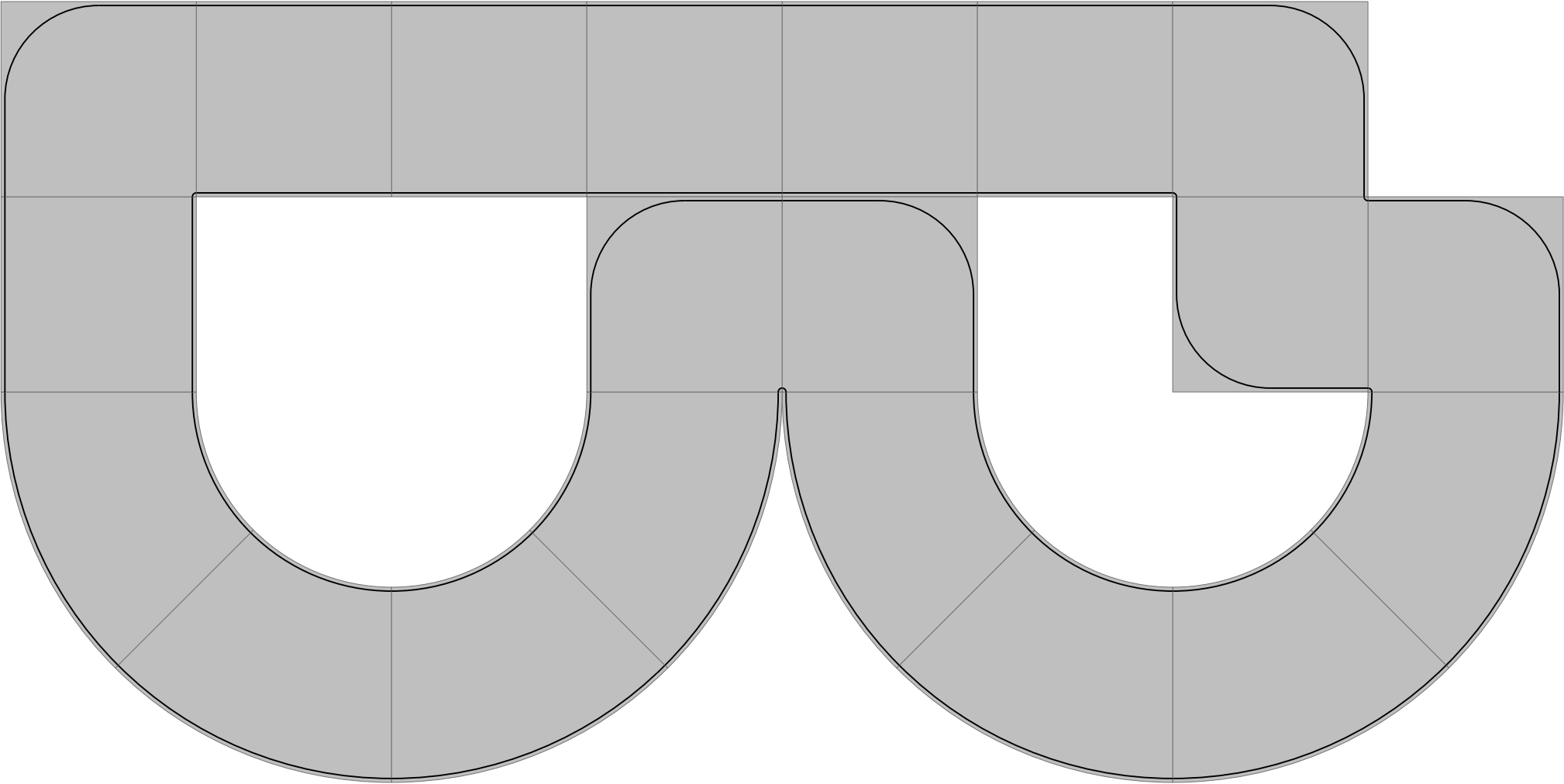
Circuit 5



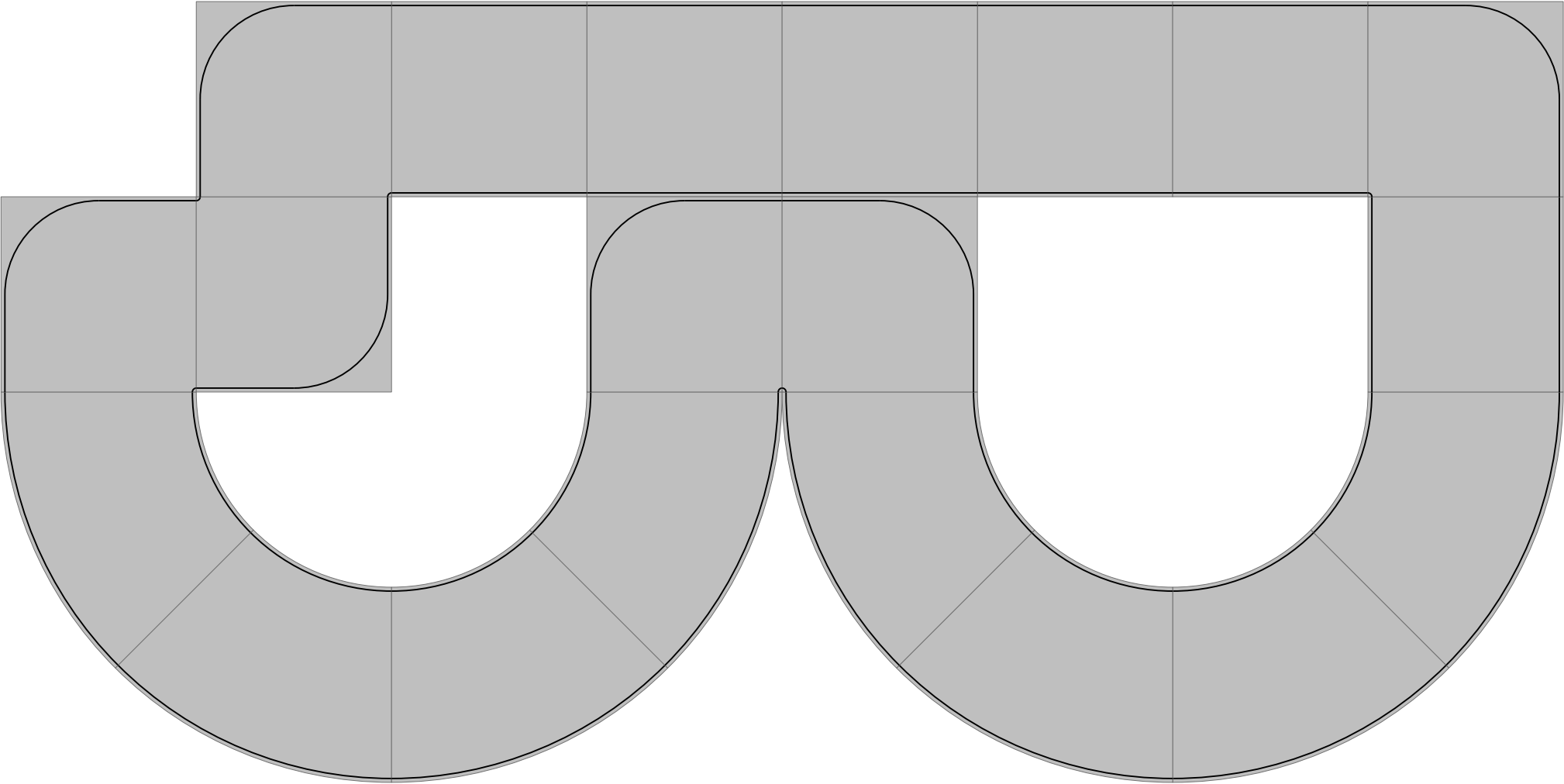
Circuit 5  
v.2

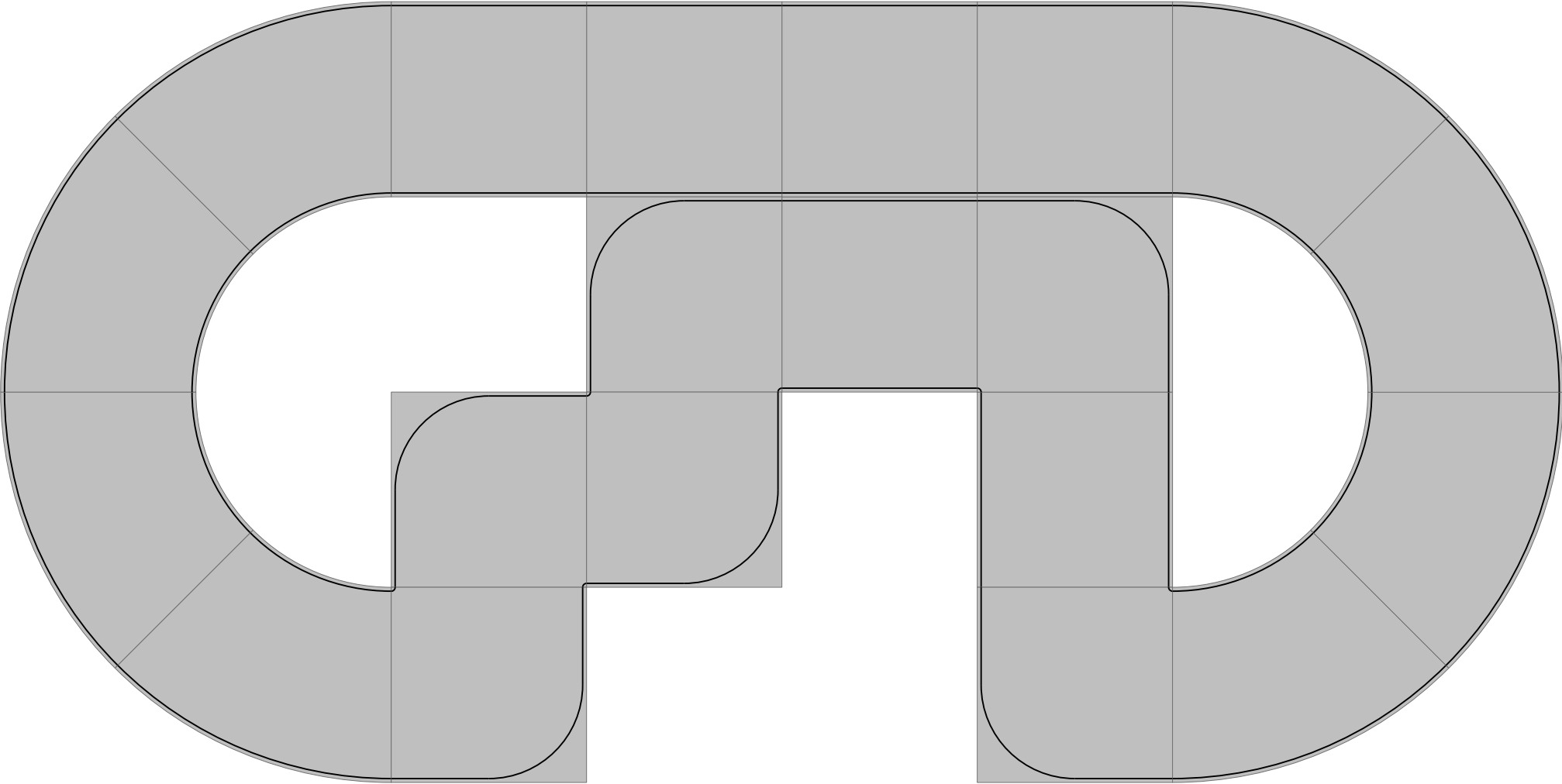


Circuit 6

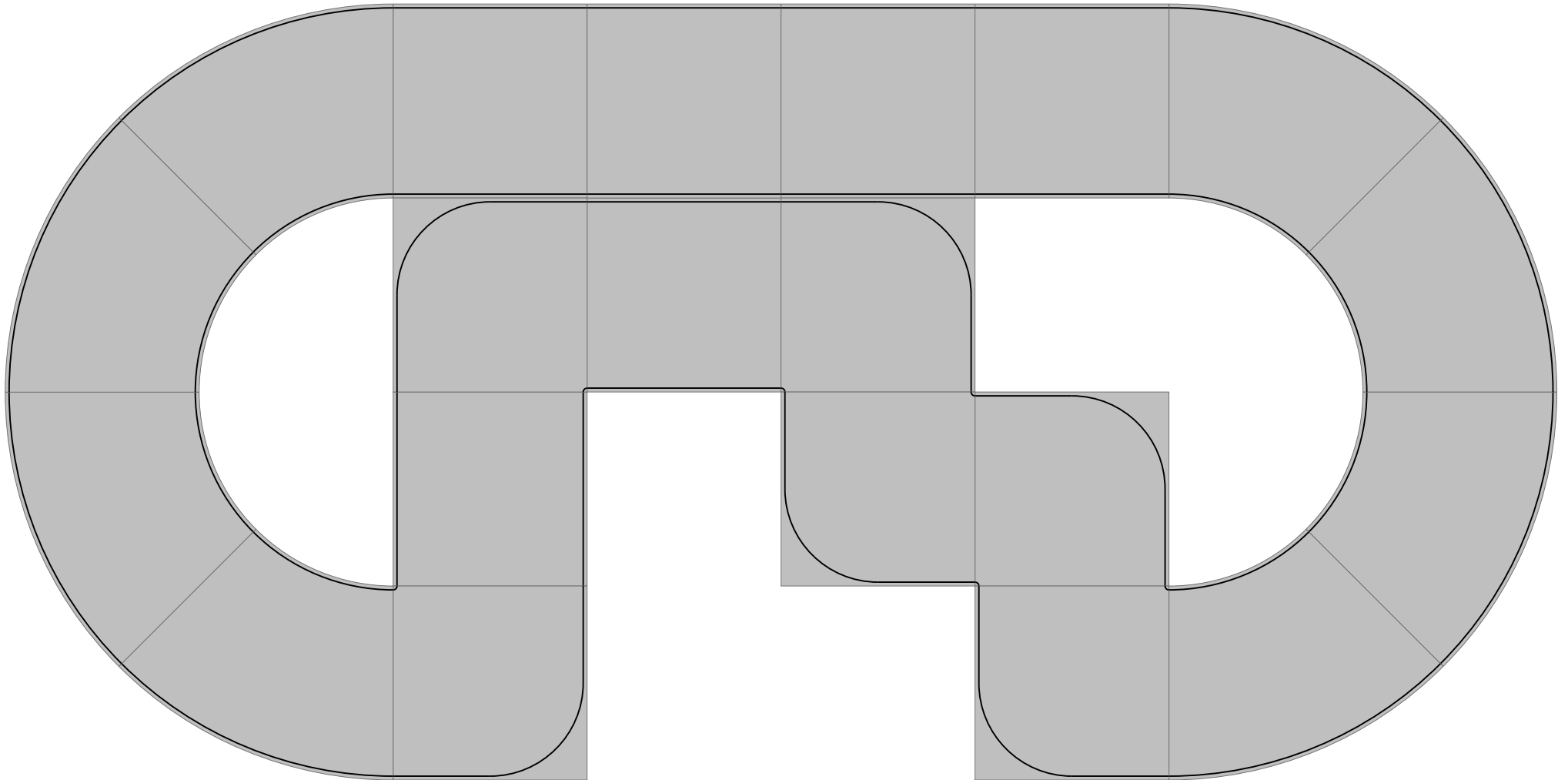


Circuit 6  
v.2

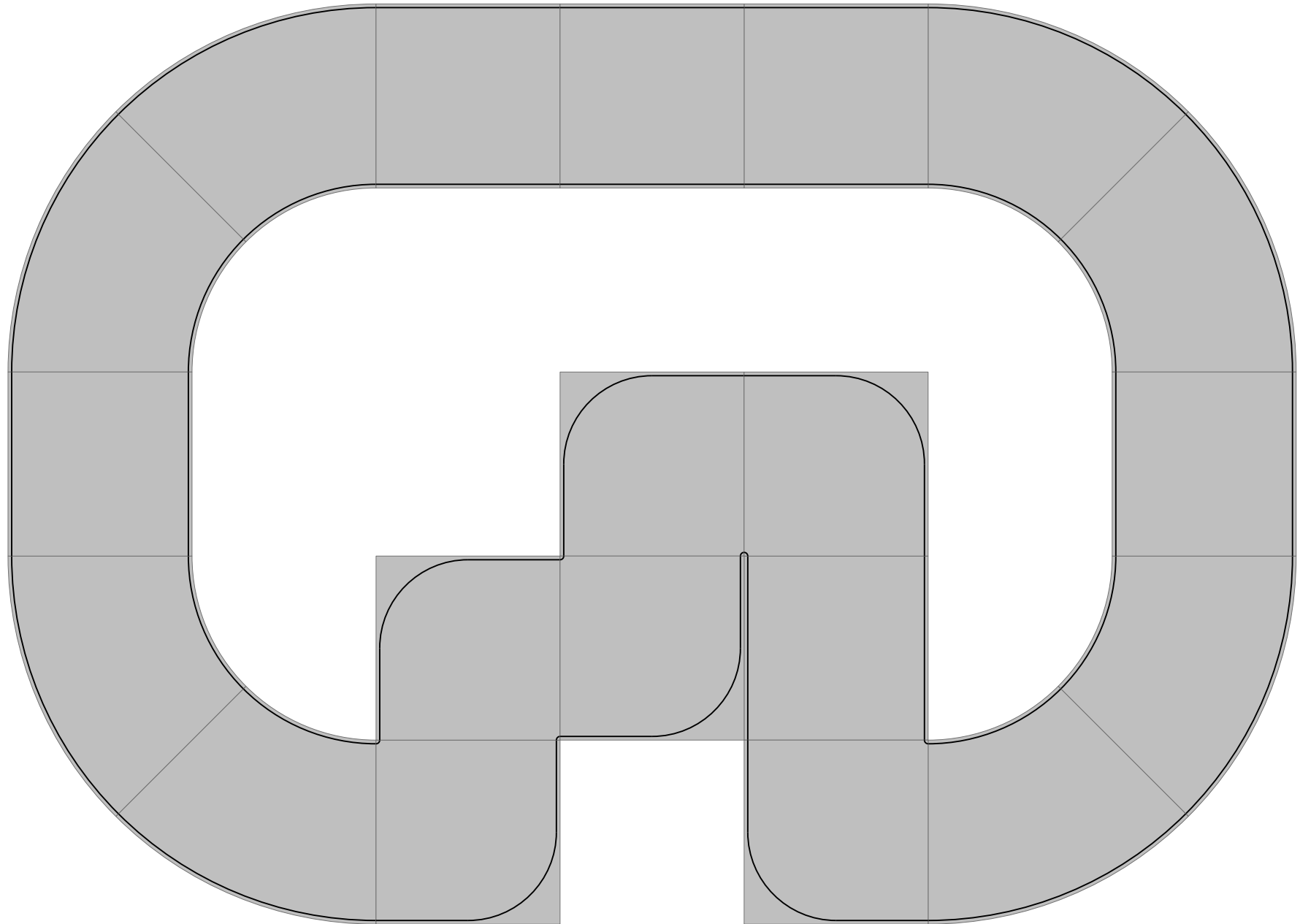




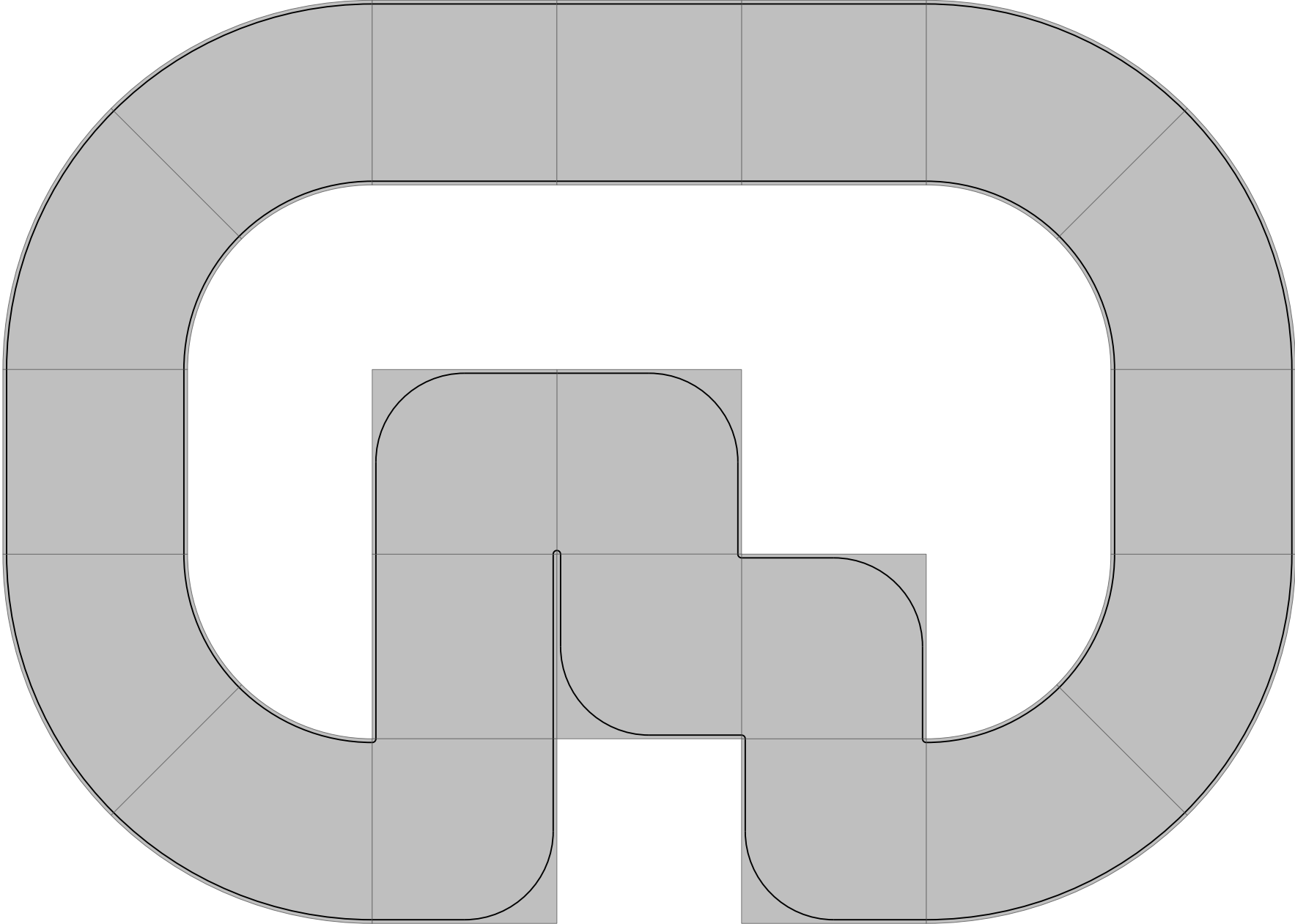
Circuit 7  
v.2



Circuit 8

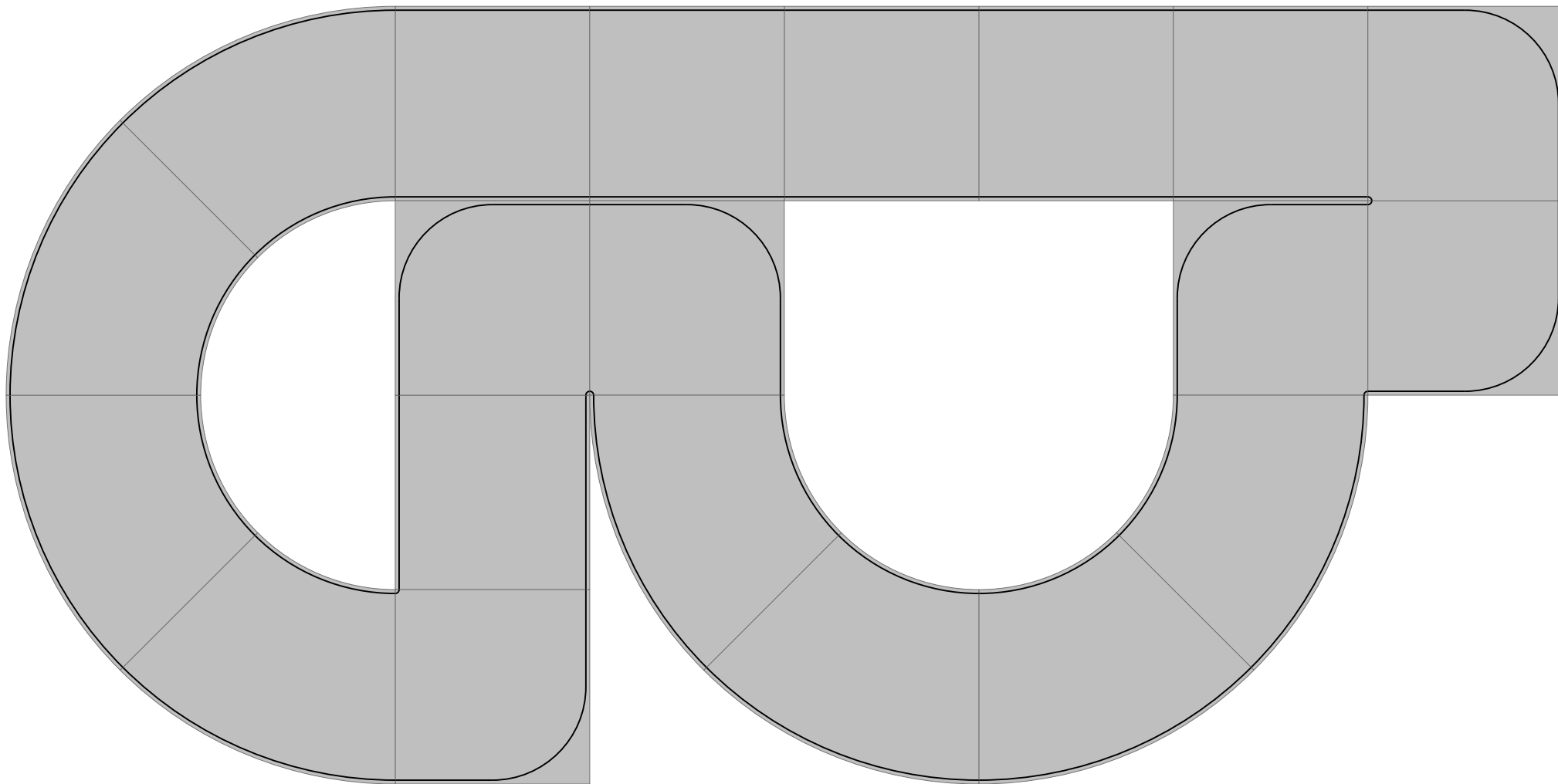


Circuit 8  
v.2

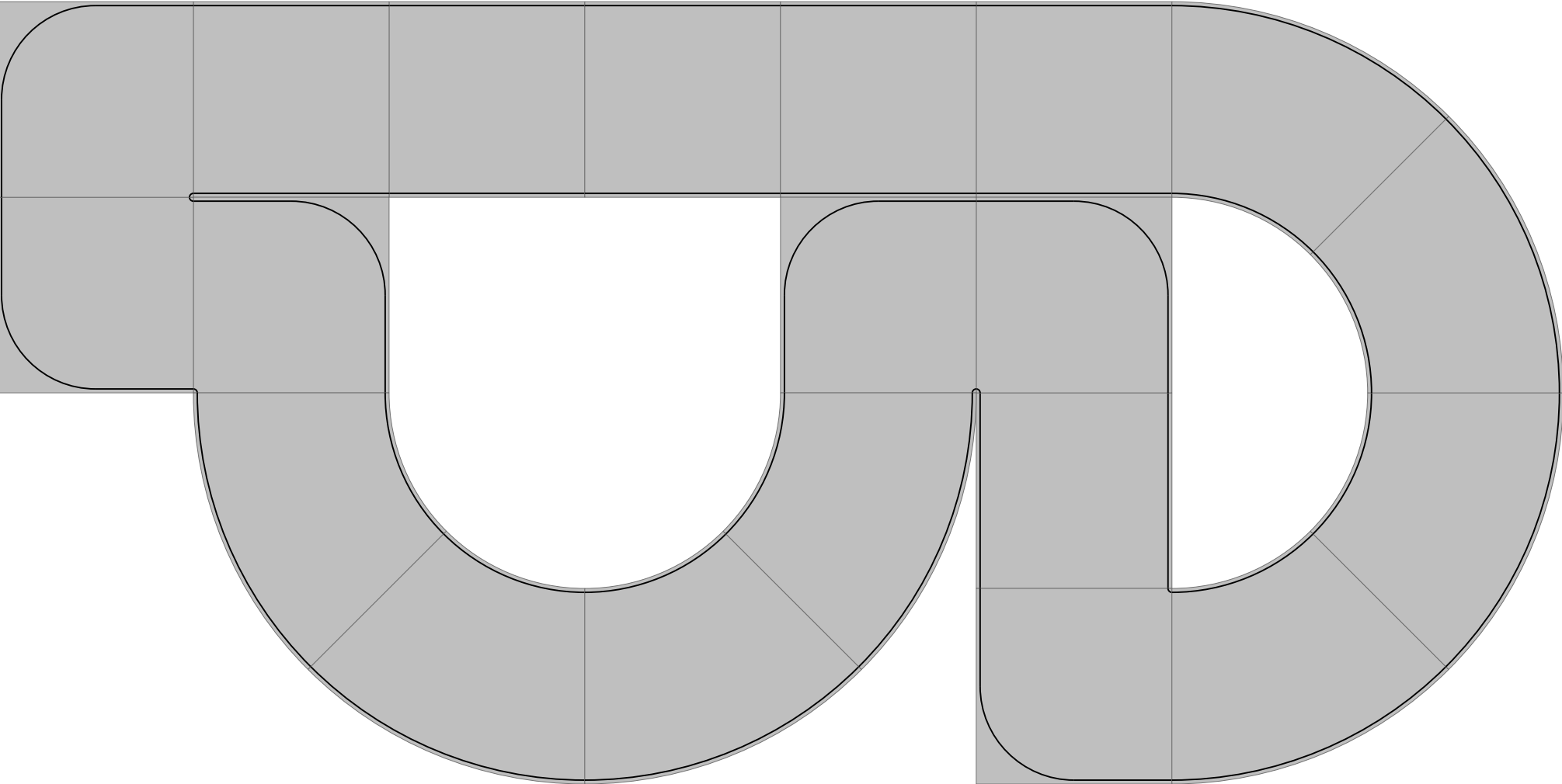


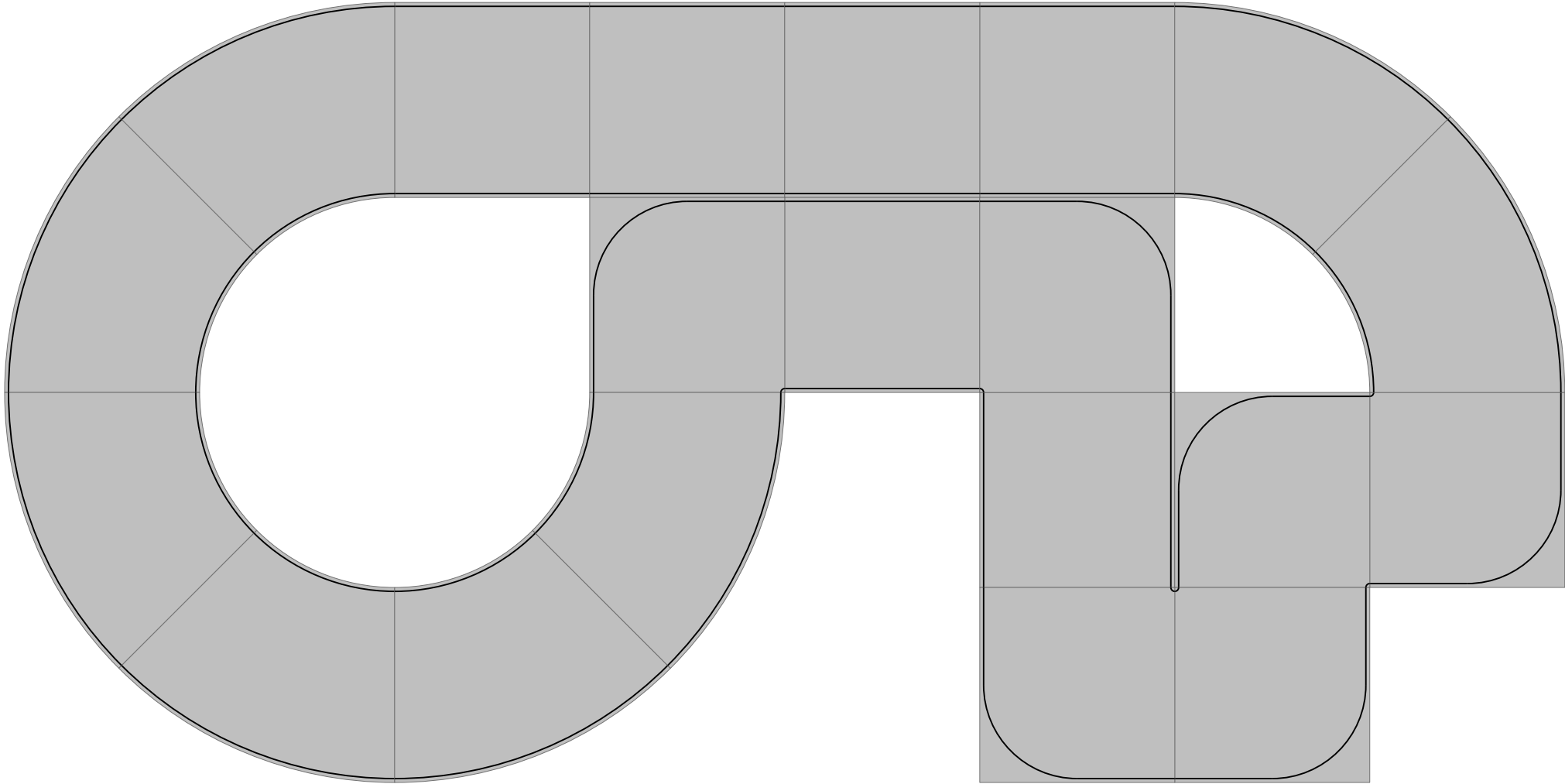


## Circuit 9

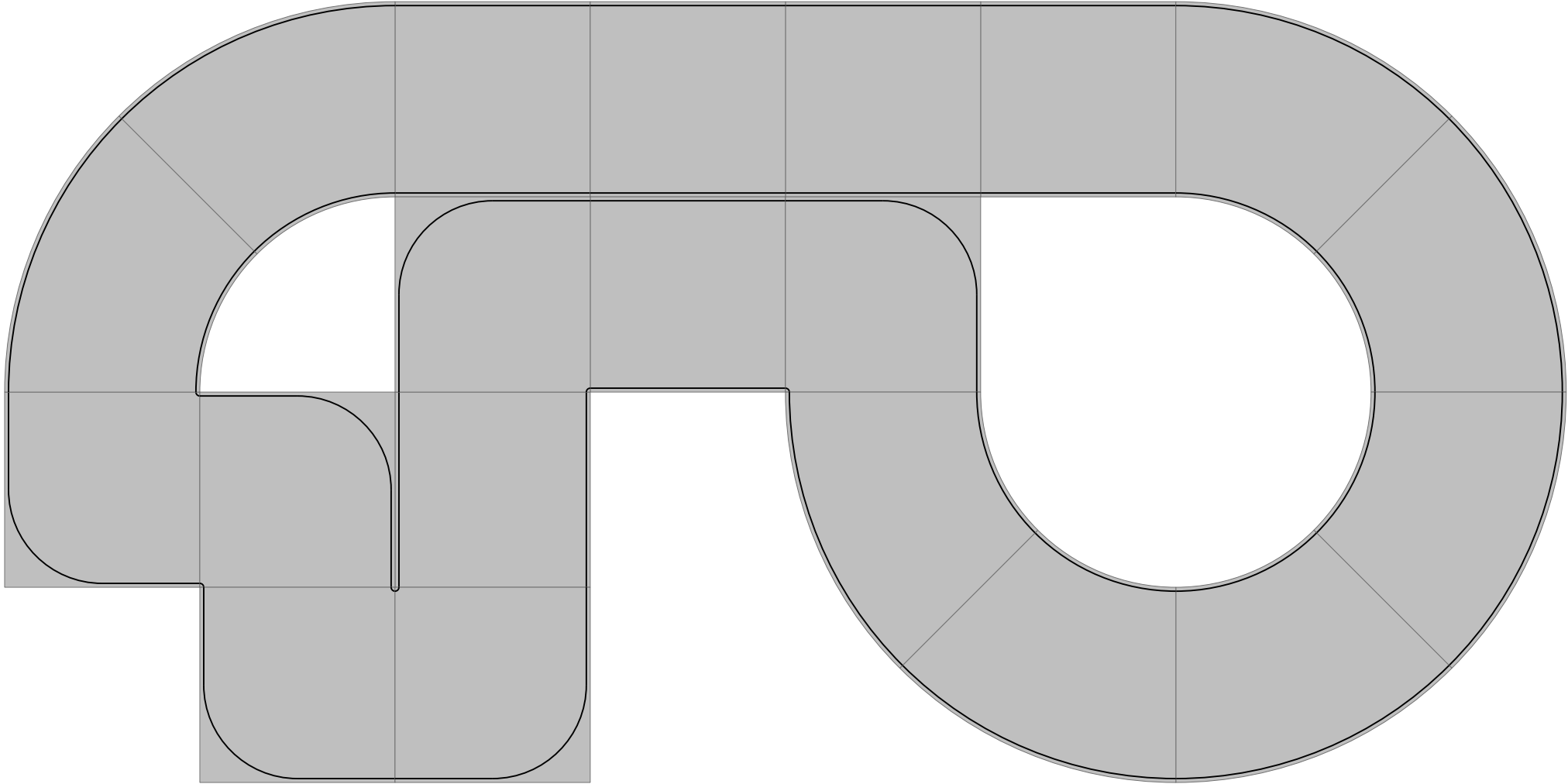


Circuit 9  
v.2

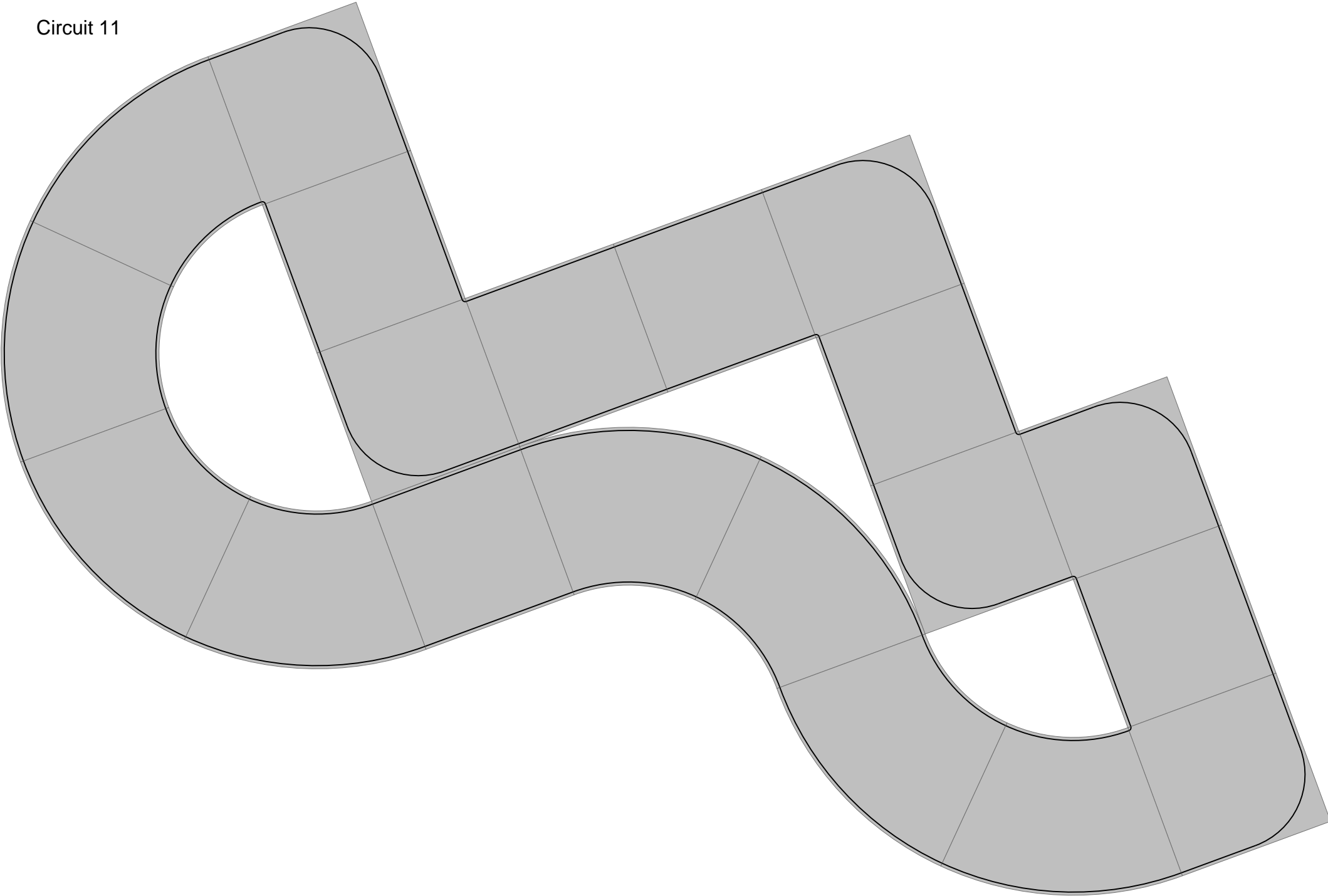




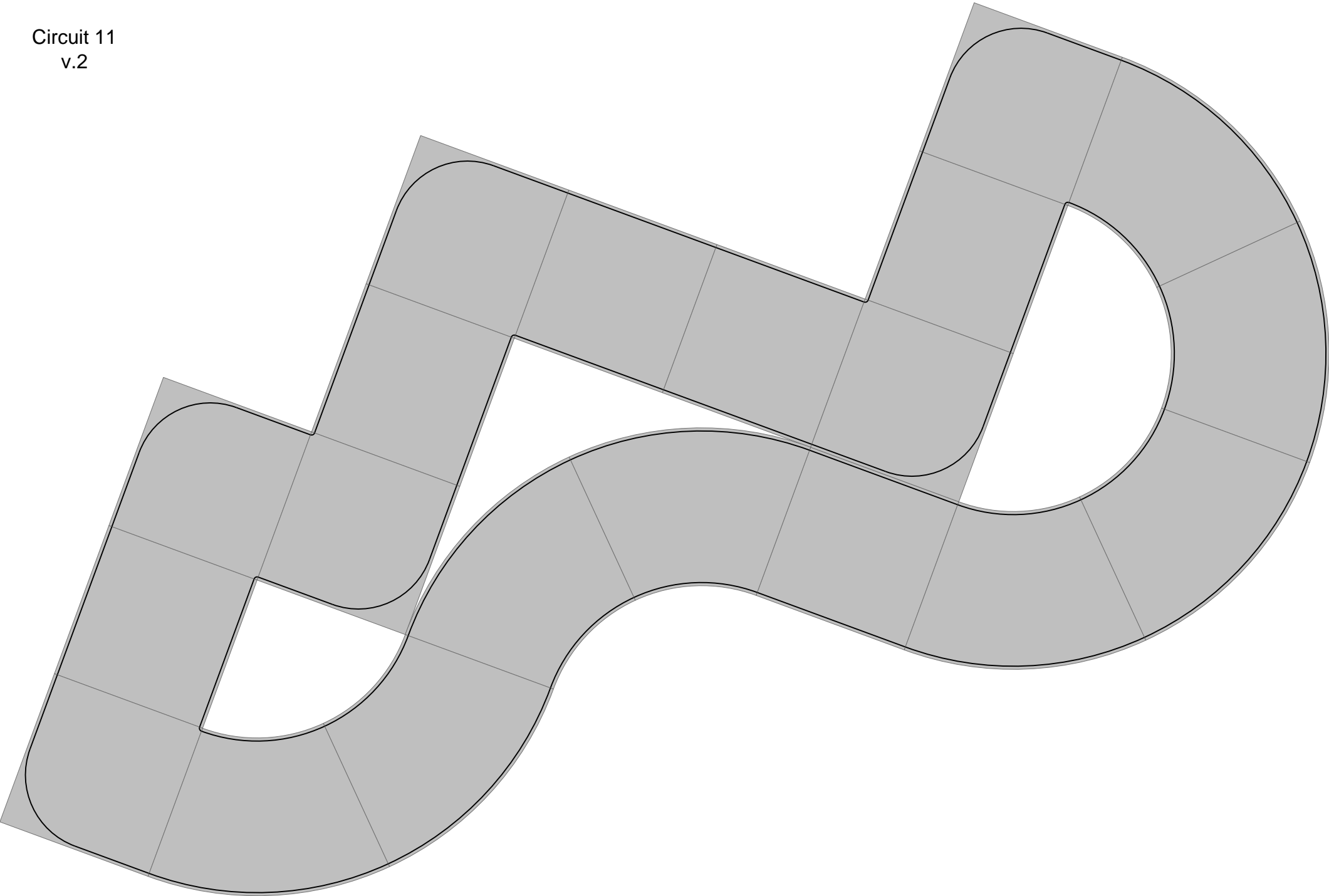
Circuit 10  
v.2



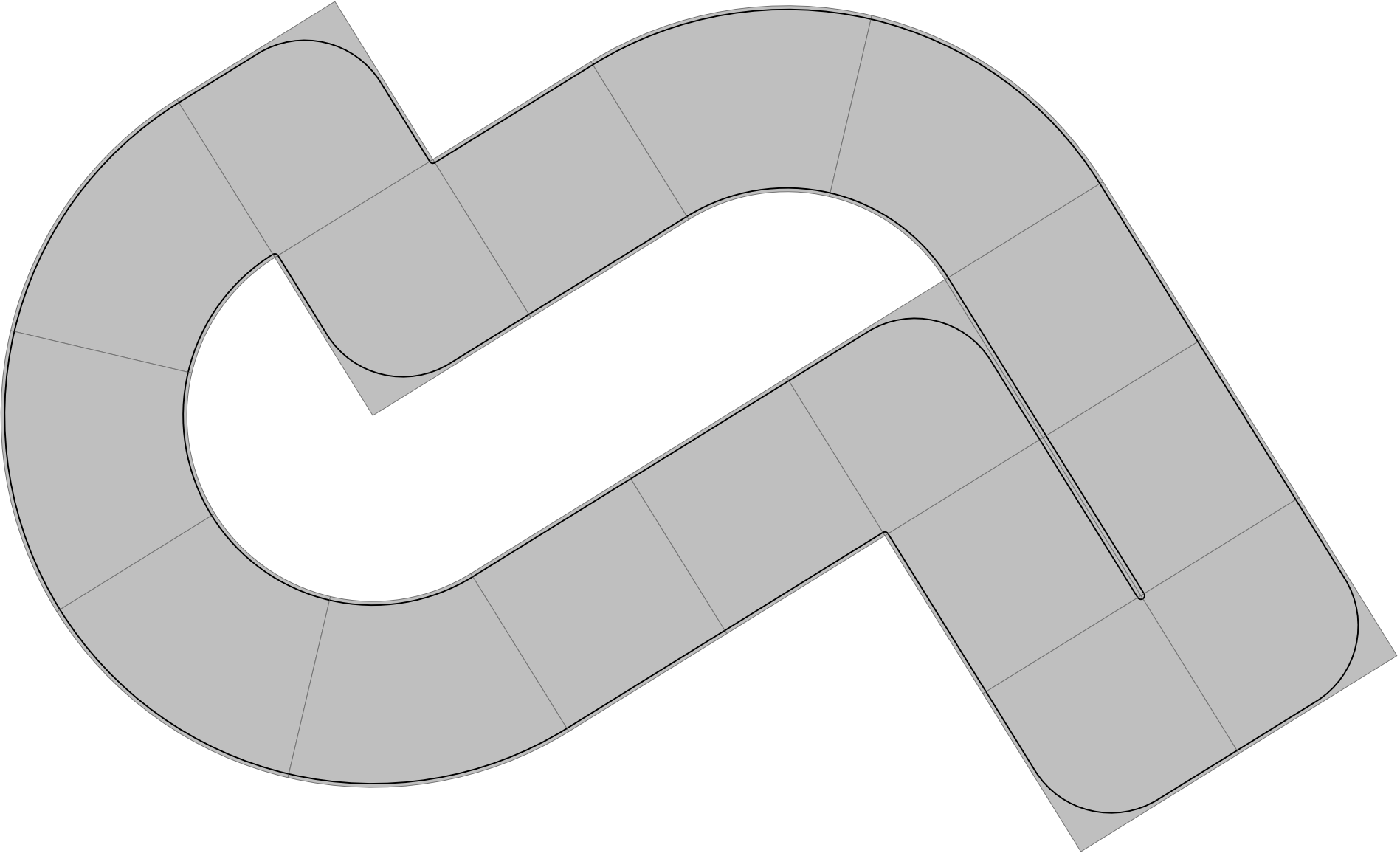
Circuit 11



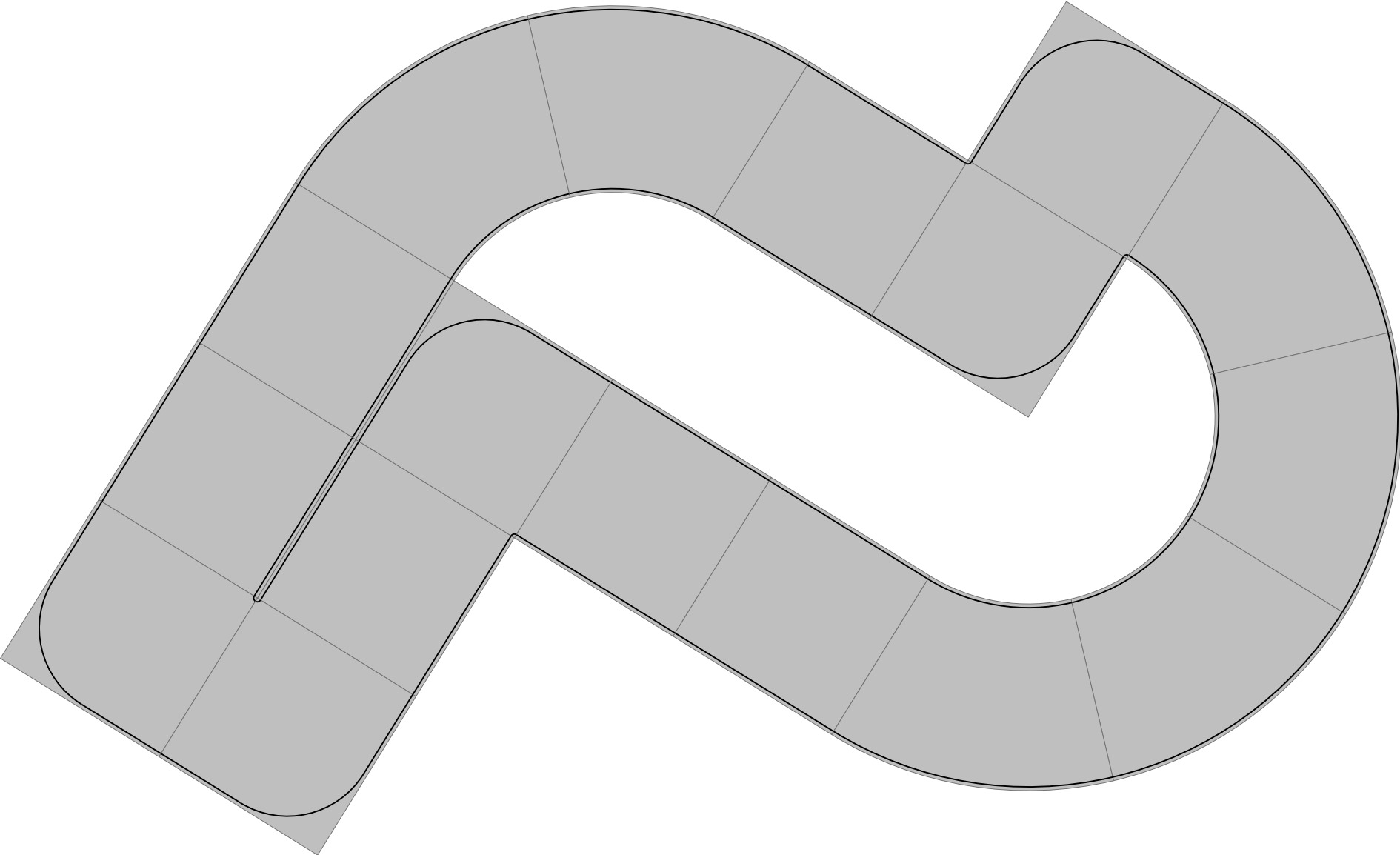
Circuit 11  
v.2



Circuit 12

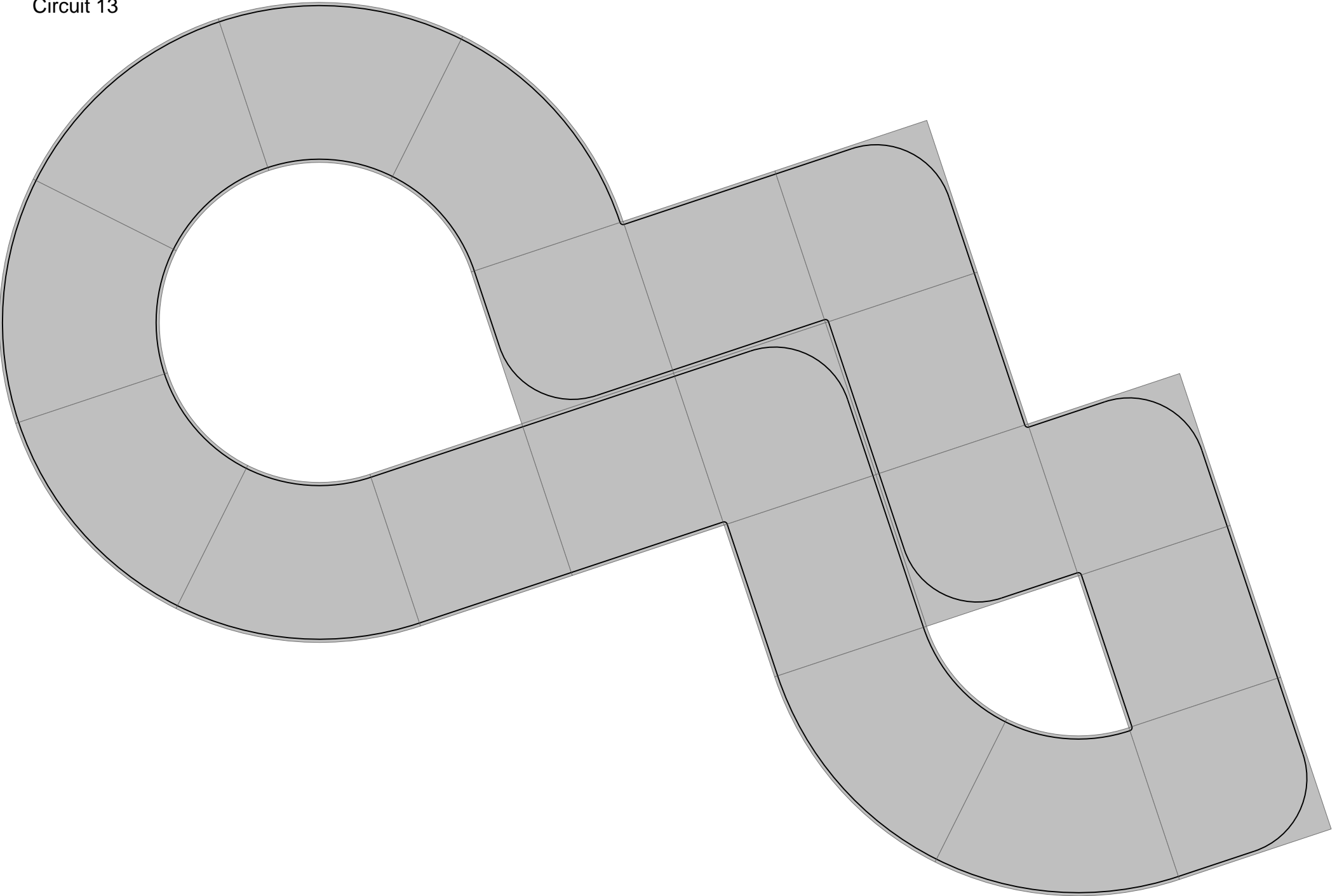


Circuit 12  
v.2

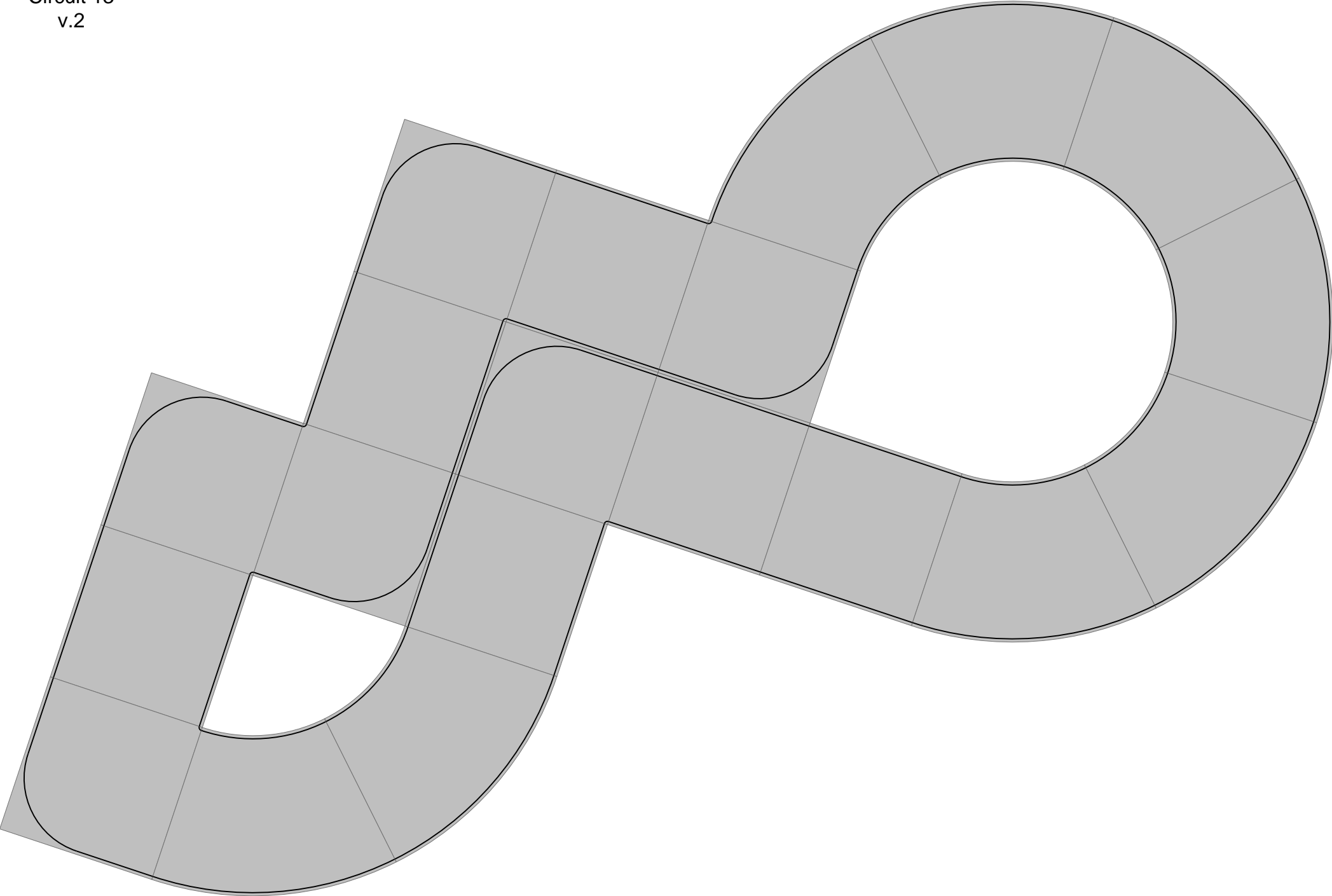




Circuit 13



Circuit 13  
v.2



Circuit 14

